ABSTRACT

An Electronic Timepiece with Analog Display and a Method of Operating Same

An electric timepiece is disclosed as including a micro-controlling with a volatile RAM adapted to be written with time-related data, an EEPROM or FRAM adapted to be written with time-related data, and a stepping motor operatively associated with an analog time display, in which the time-related data in the volatile RAM and in the EEPROM or FRAM are synchronized with each other, and the micro-controller is adapted to update the time-related data in the volatile RAM and in the EPPROM or FRAM simultaneously every second.

[Fig. 1]

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